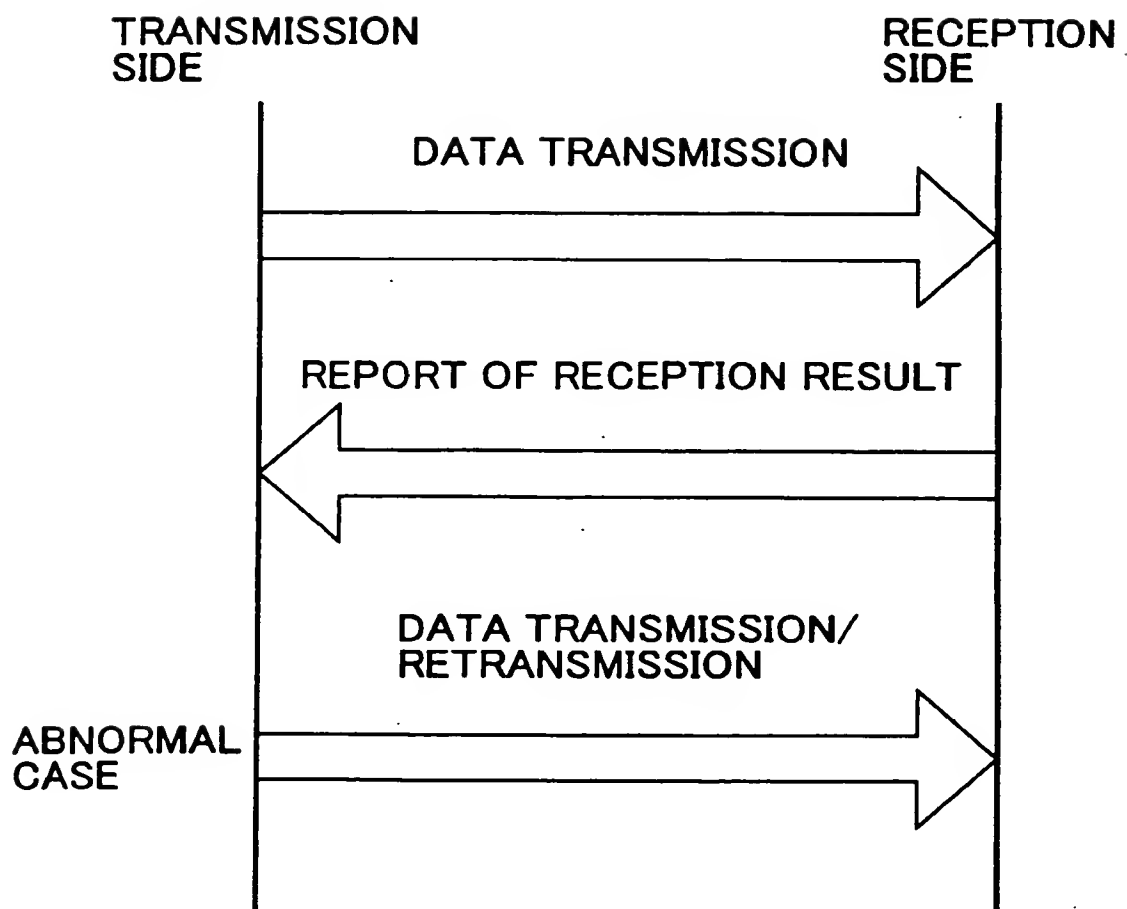


1/10  
**FIG.1**



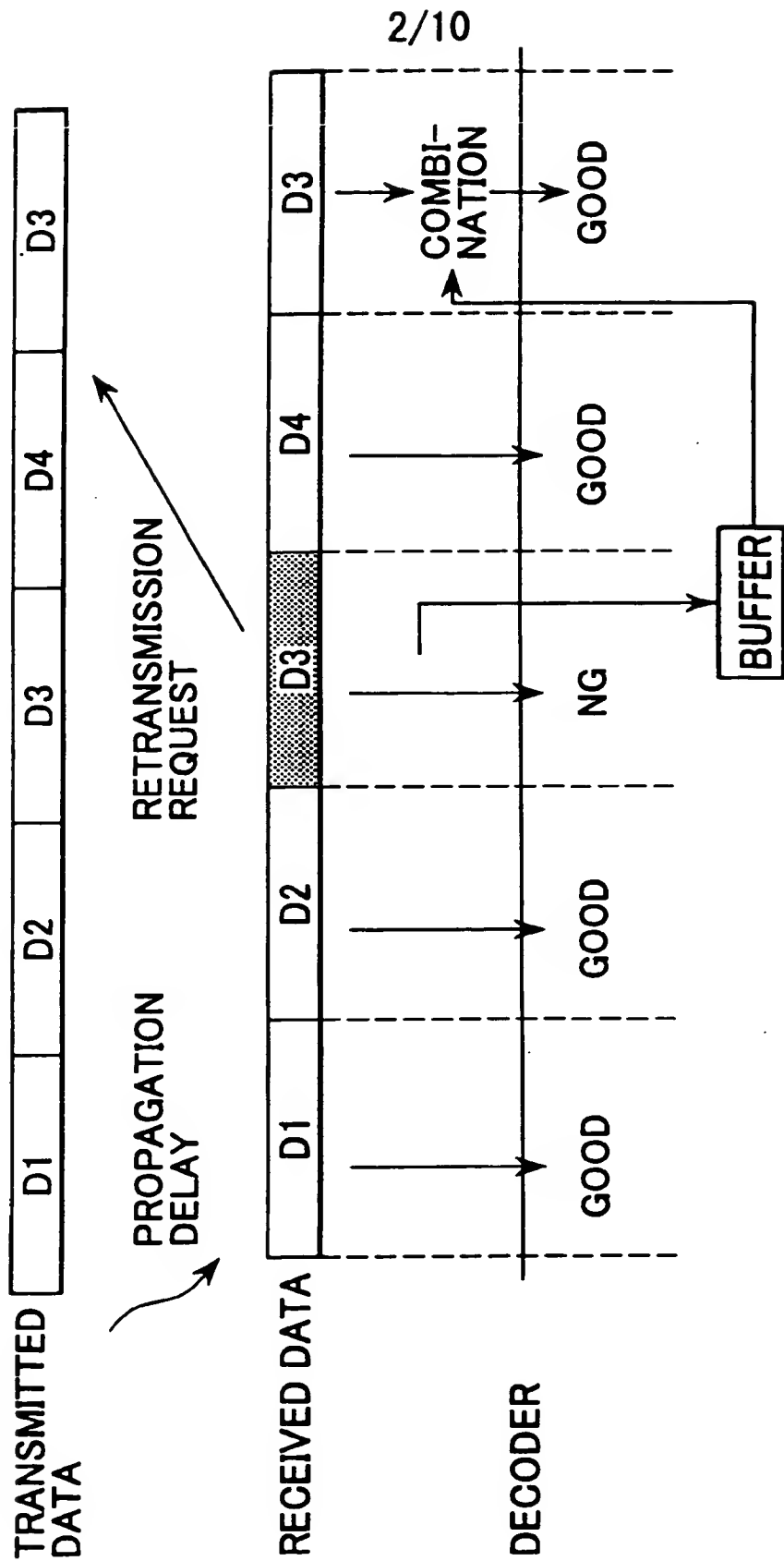


FIG.2

FIG.3A

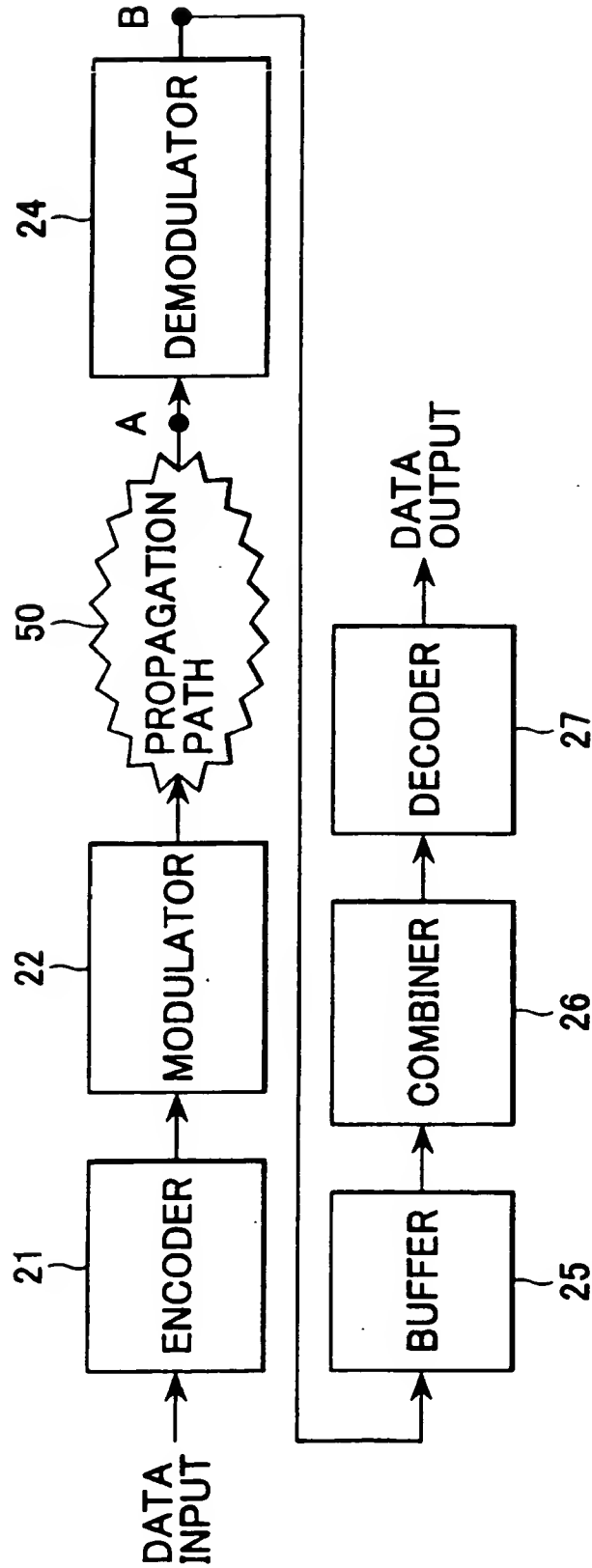


FIG.3B

MODULATION METHOD	A	B
QPSK	2(I.Q)	2 bit
8QAM	2(I.Q)	3 bit
16QAM	2(I.Q)	4 bit
64QAM	2(I.Q)	6 bit

FIG.4

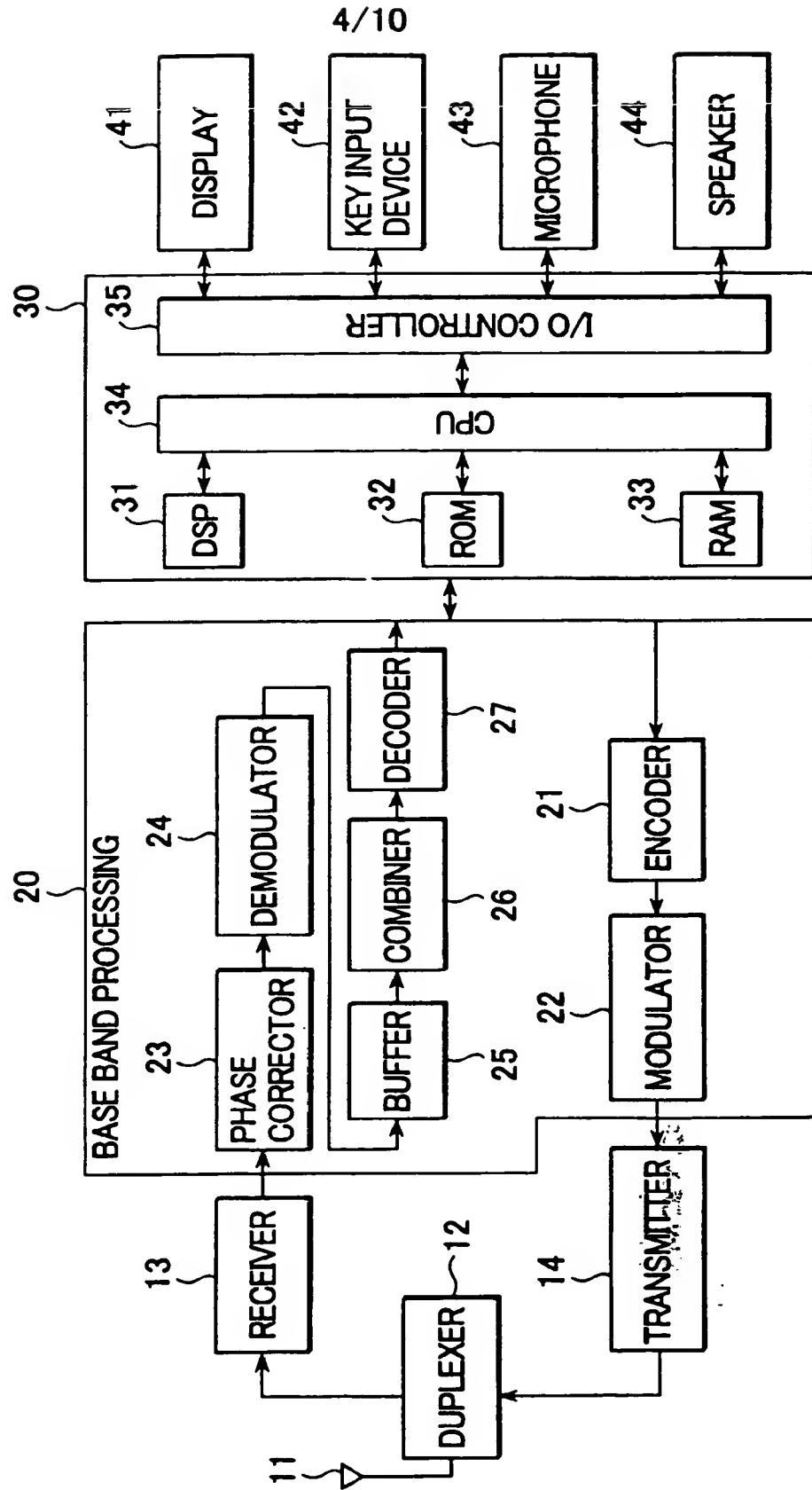


FIG.5A

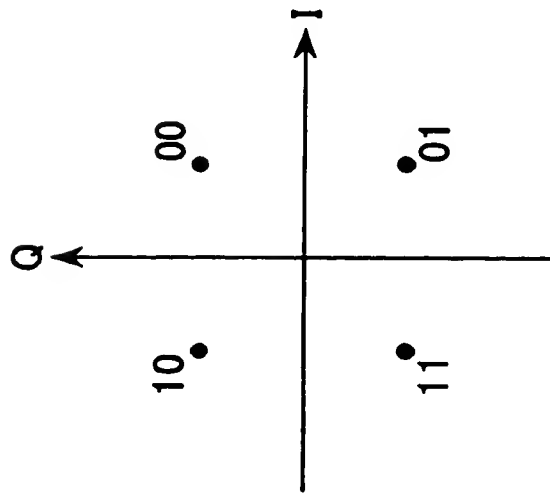


FIG.5A

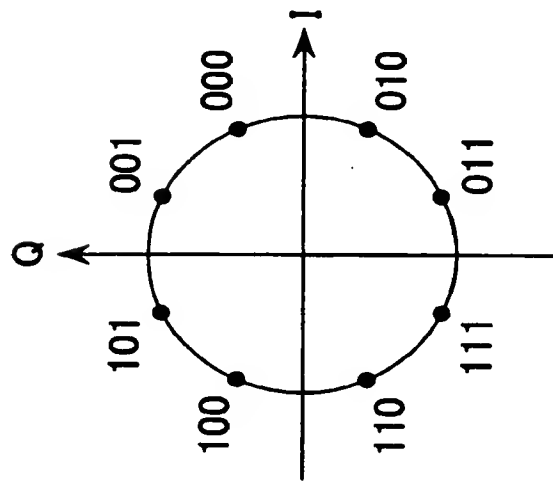


FIG.5A

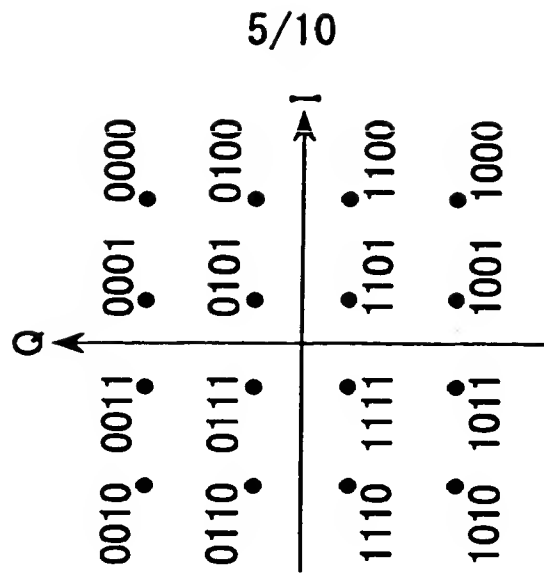


FIG.6

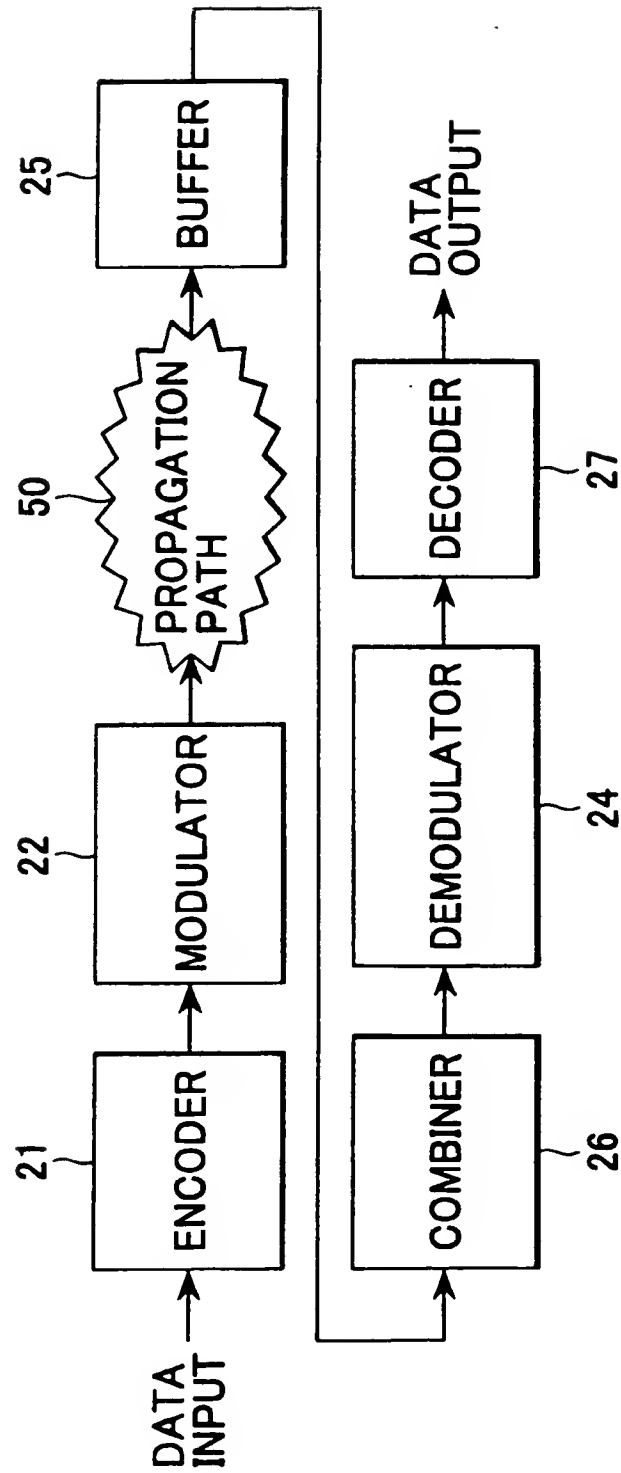


FIG. 7

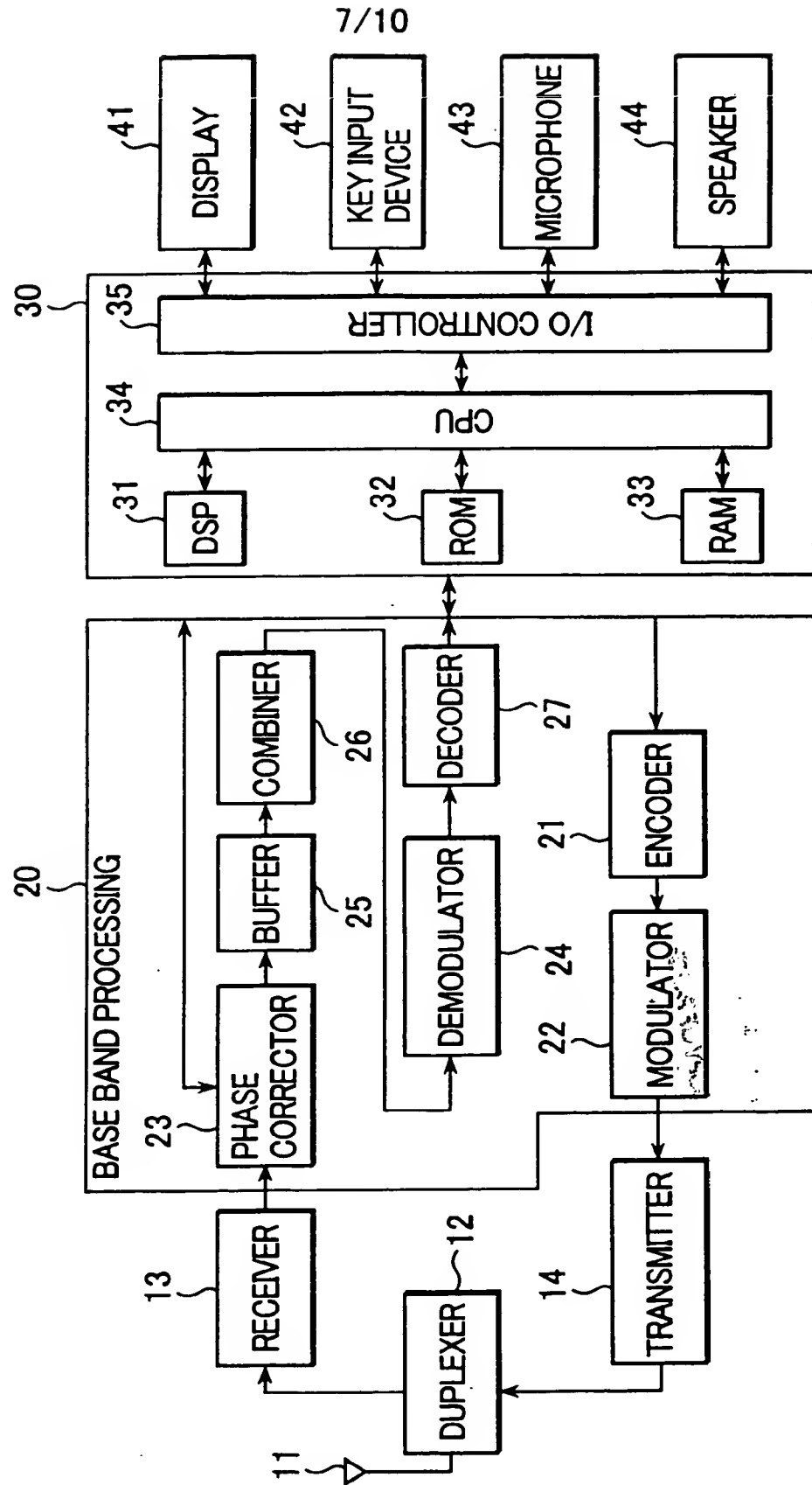


FIG.8

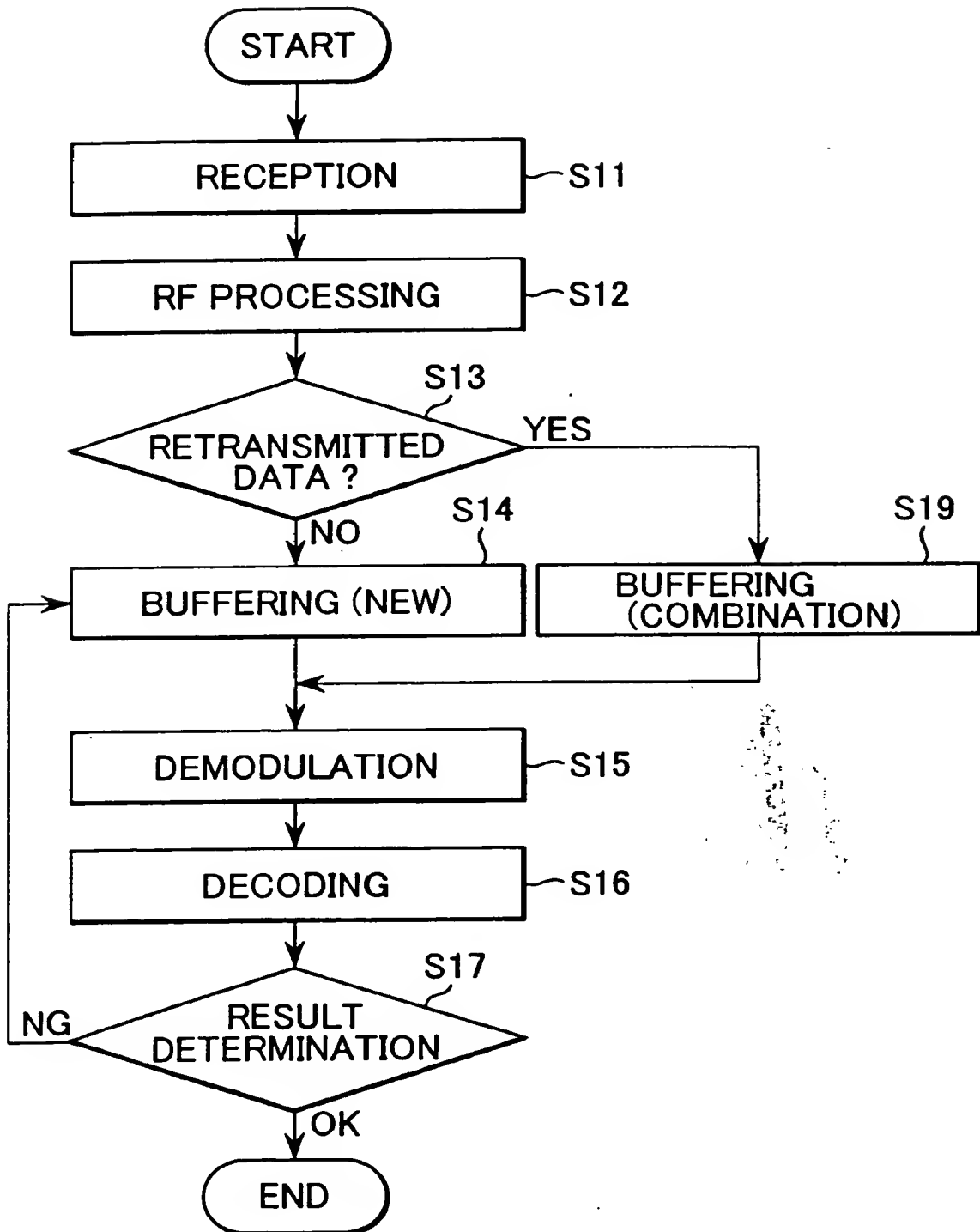
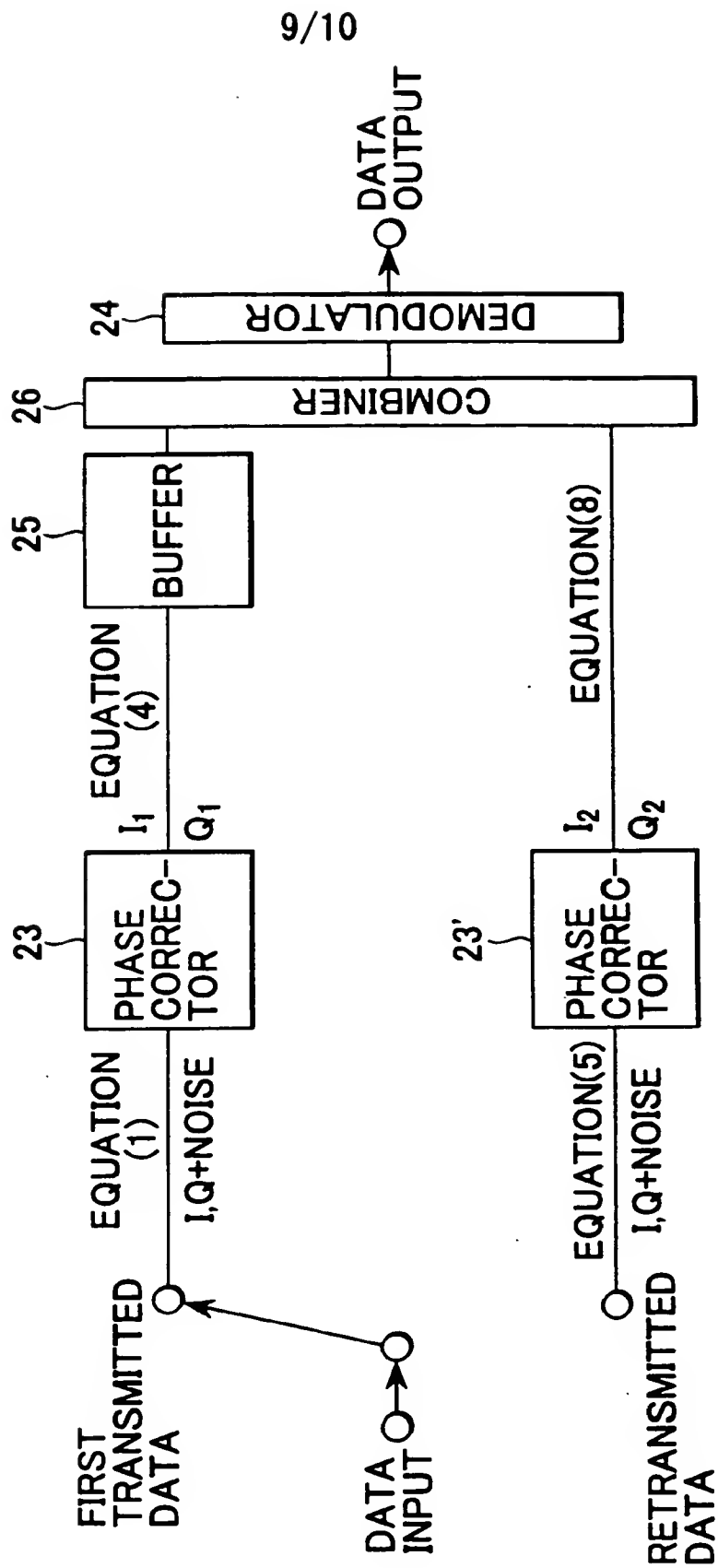


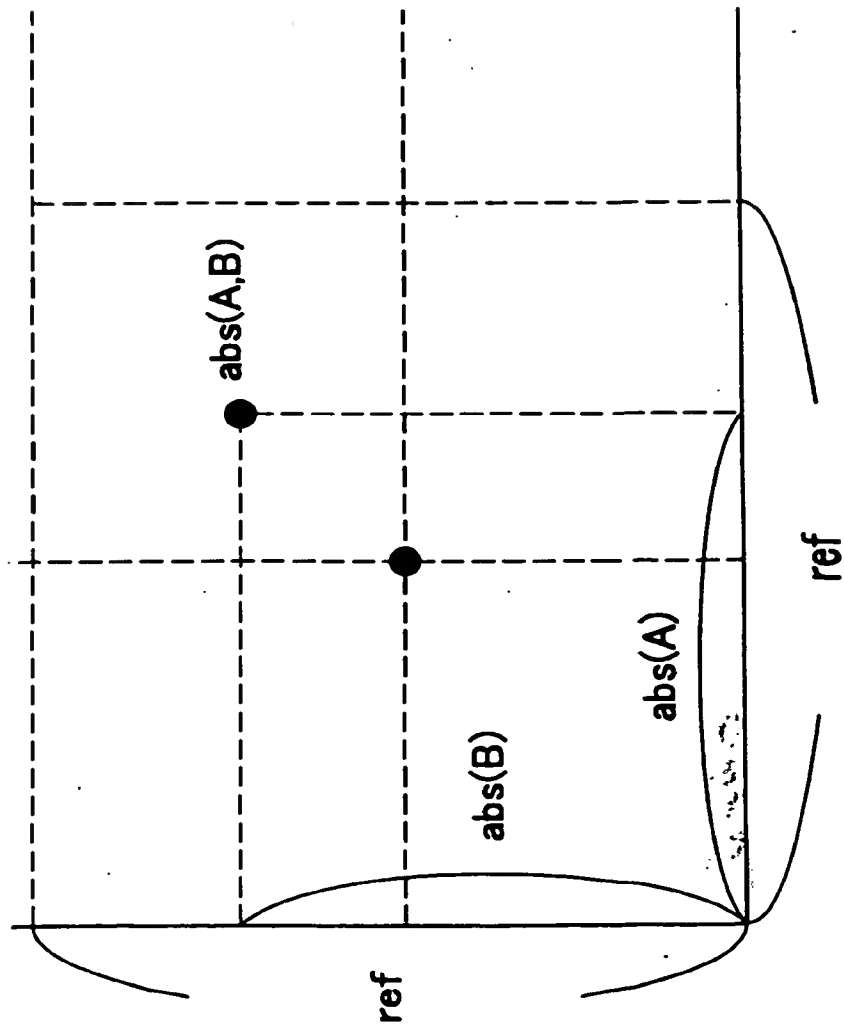


FIG. 9



DATA INPUT

FIG.10



$$b0 = B$$

$$b1 = \text{abs}(B) - \text{ref}$$

$$b2 = A$$

$$b3 = \text{abs}(A) - \text{ref}$$